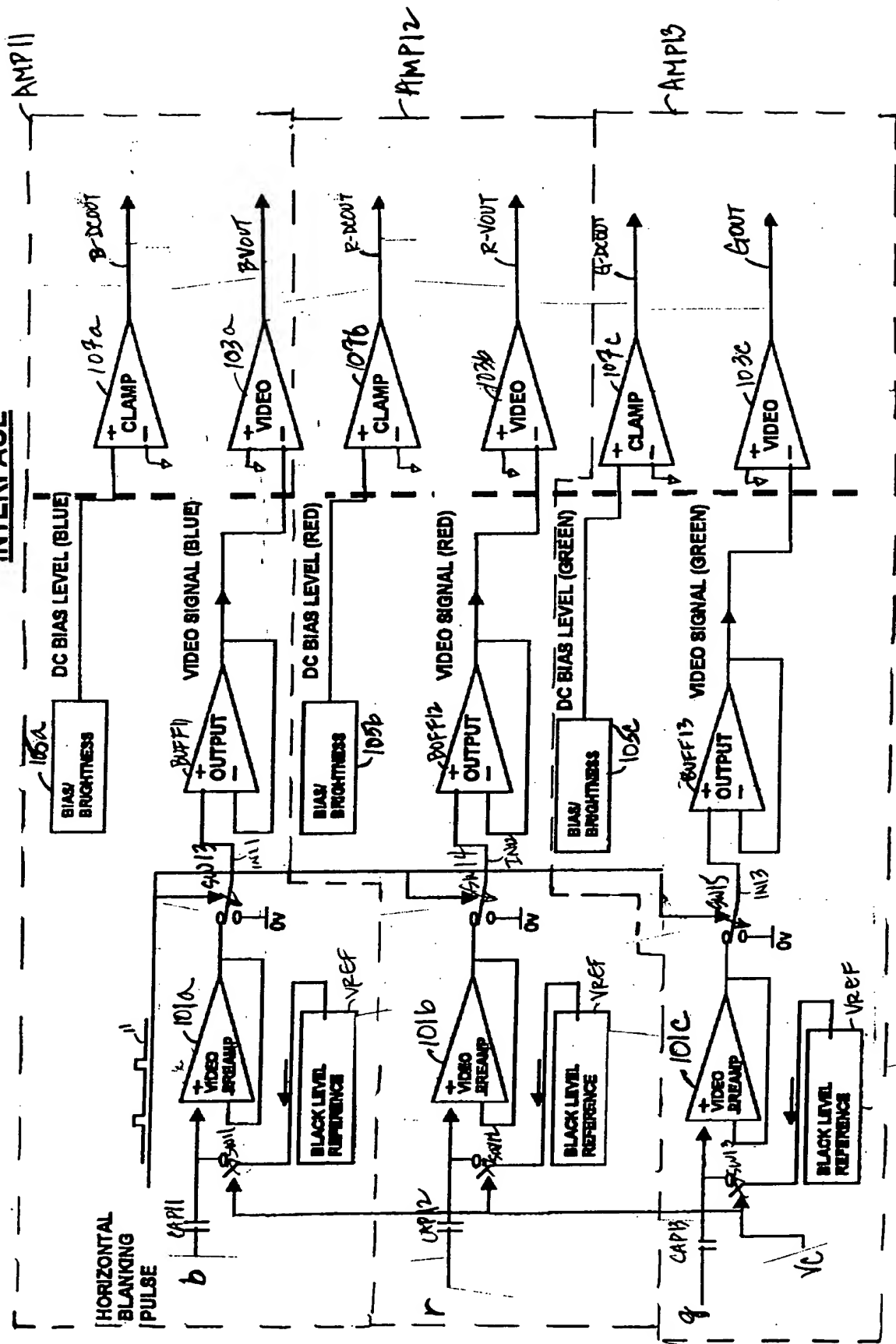


# INTERFACE



100

FIGURE 1

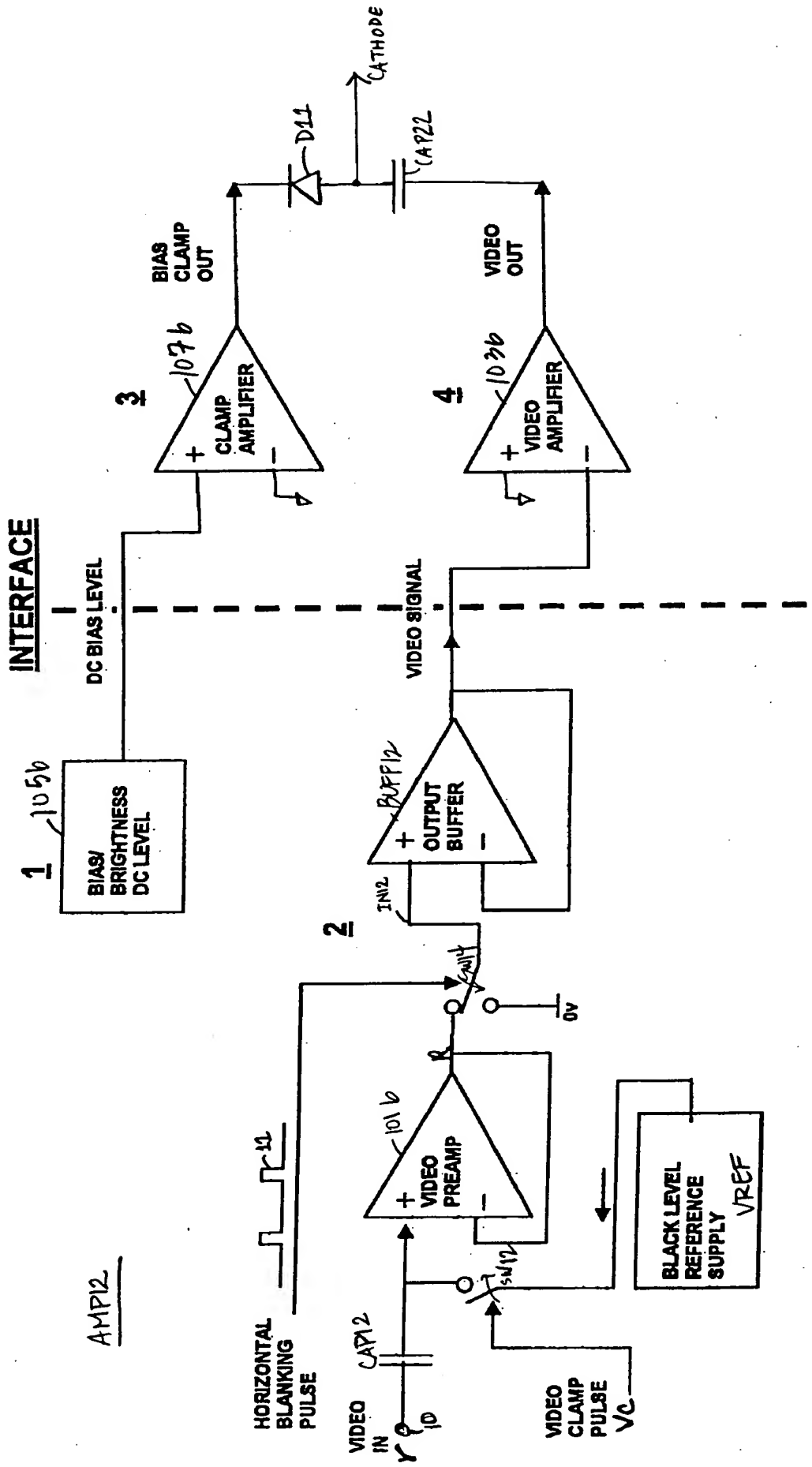


FIGURE 2



## TYPICAL DISPLAY VIDEO SYSTEM

# ARC PROTECTION,

## ESD. INPUT

## TERMINATION

**ETC NOT SHOWN**

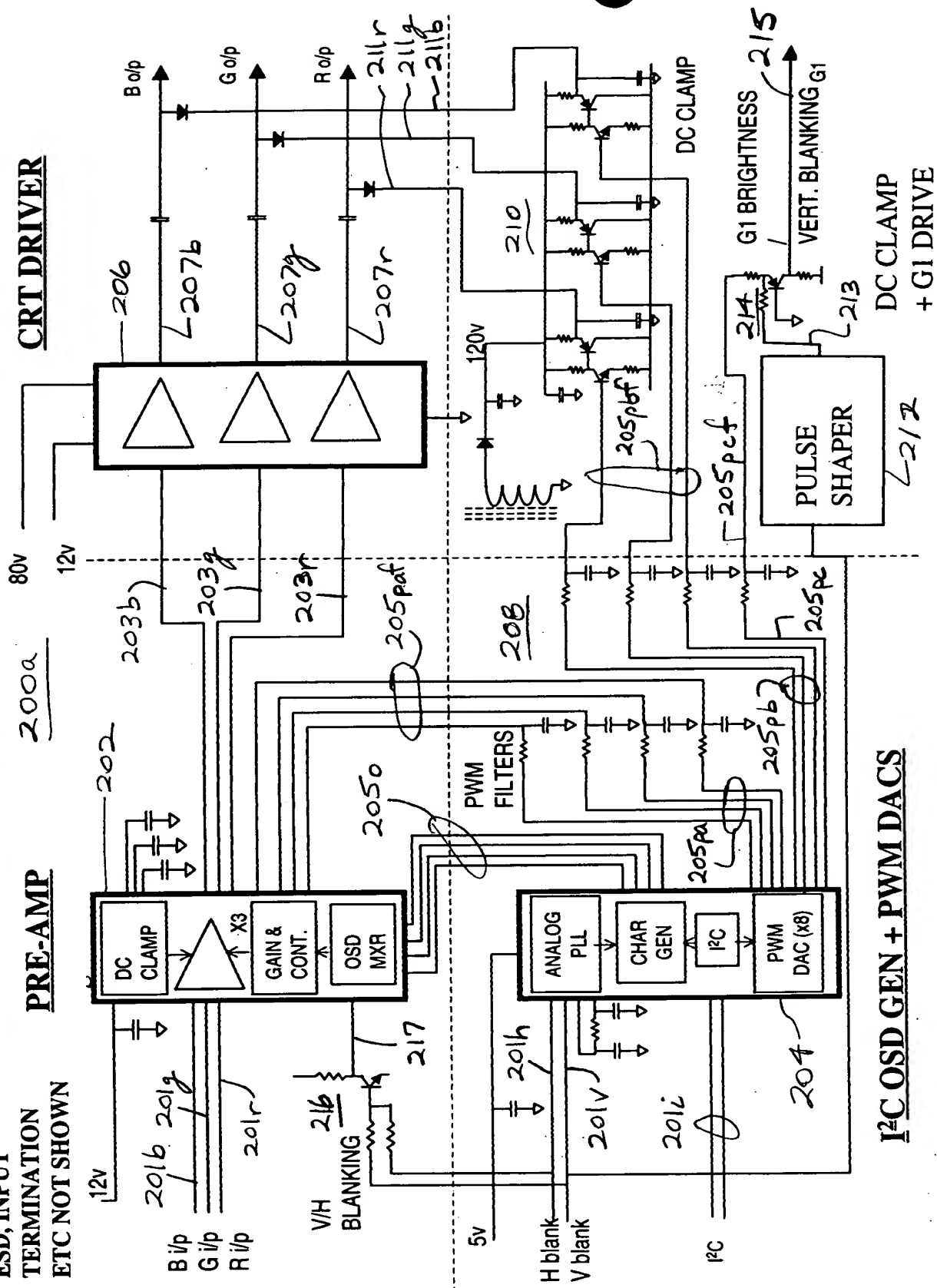


FIGURE 3 (PRIOR ART)



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AG2DC

# DC COUPLED VIDEO SYSTEM

ARC PROTECTION,  
ESD, INPUT  
TERMINATION  
ETC NOT SHOWN

## PRE-AMP

## DC COUPLED CRT DRIVER

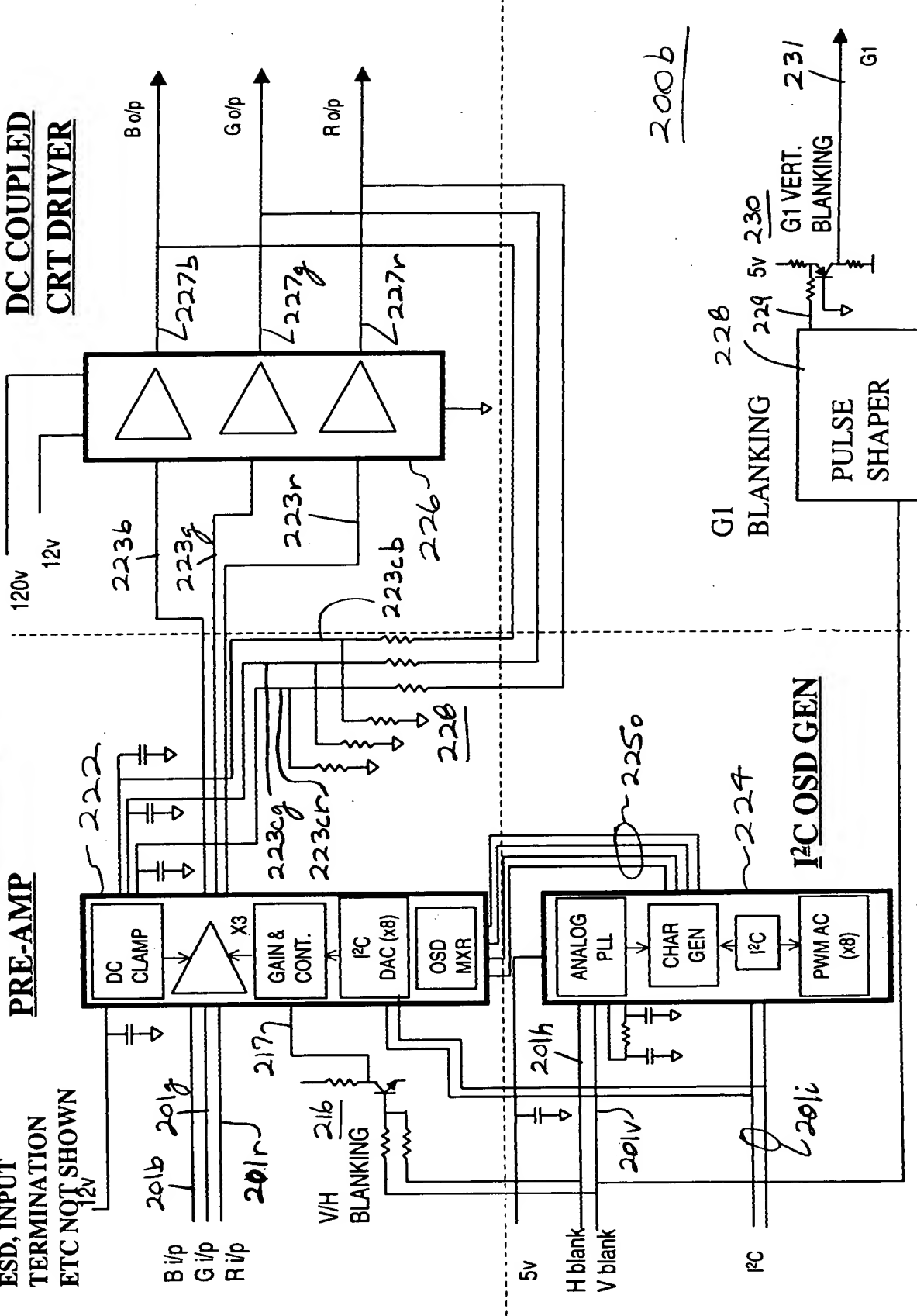


FIGURE 4 (PRIOR ART)

007201 66486960

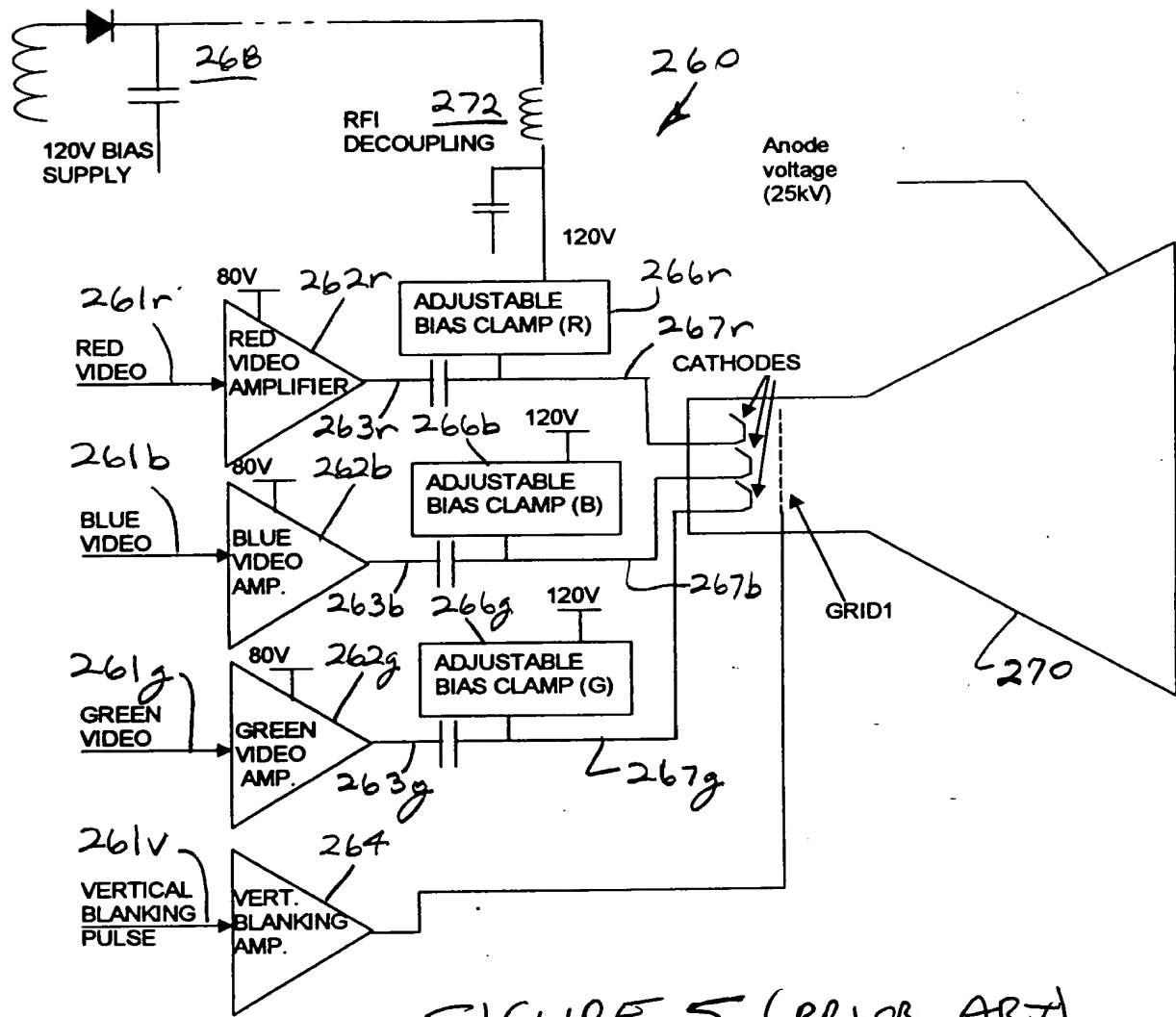


FIGURE 5 (PRIOR ART)

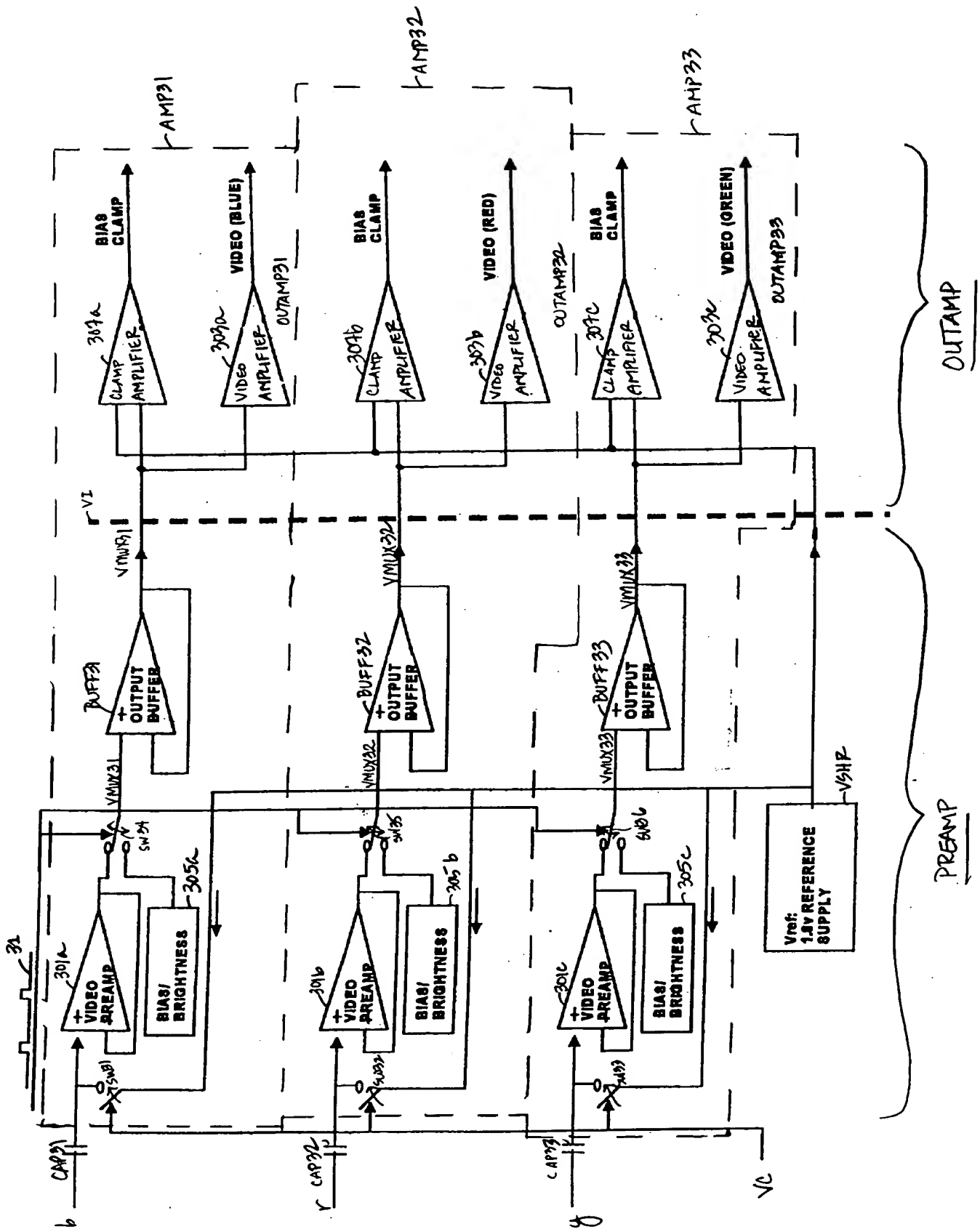
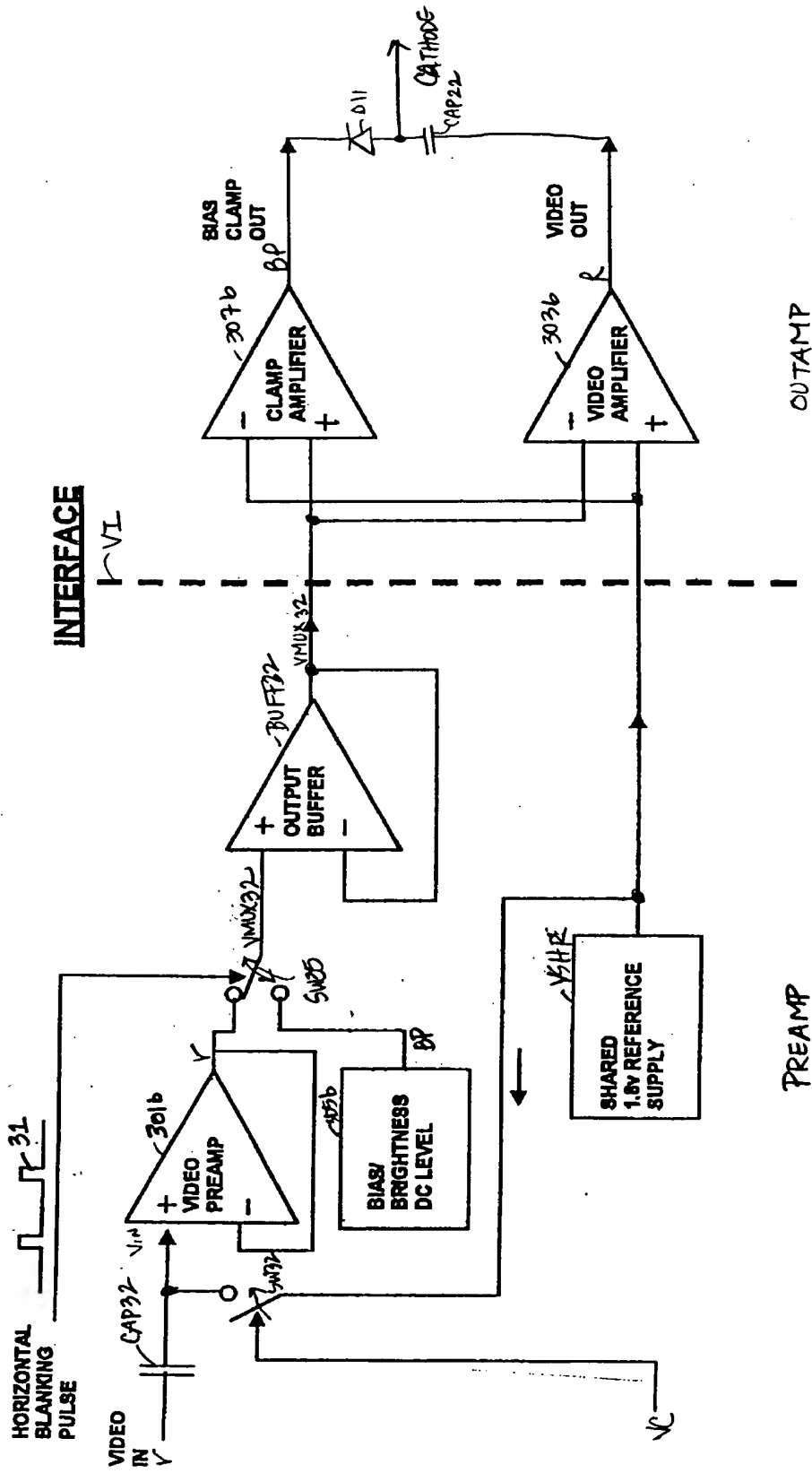


FIGURE 6



AMP32

FIGURE 7

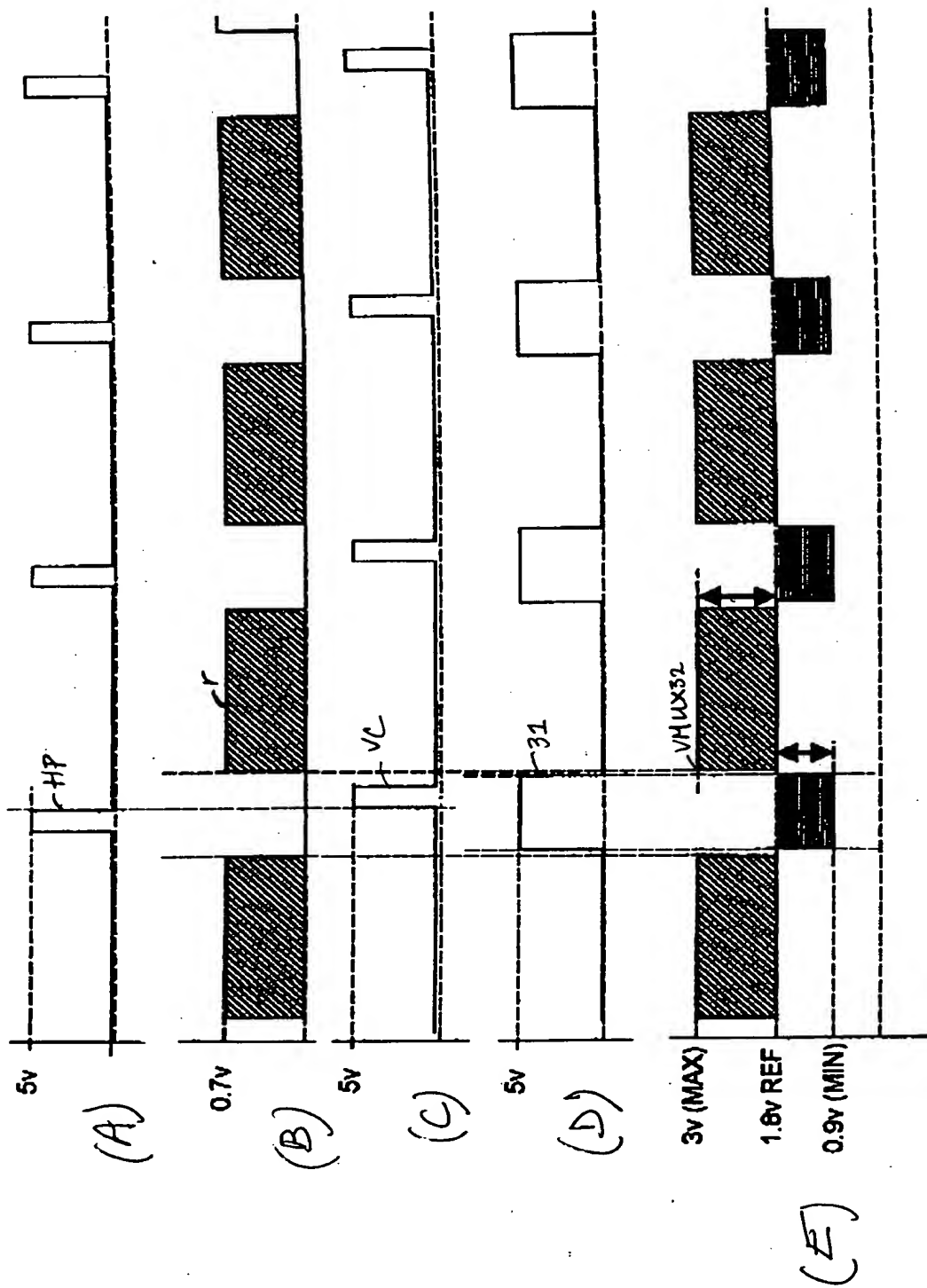


FIGURE 8



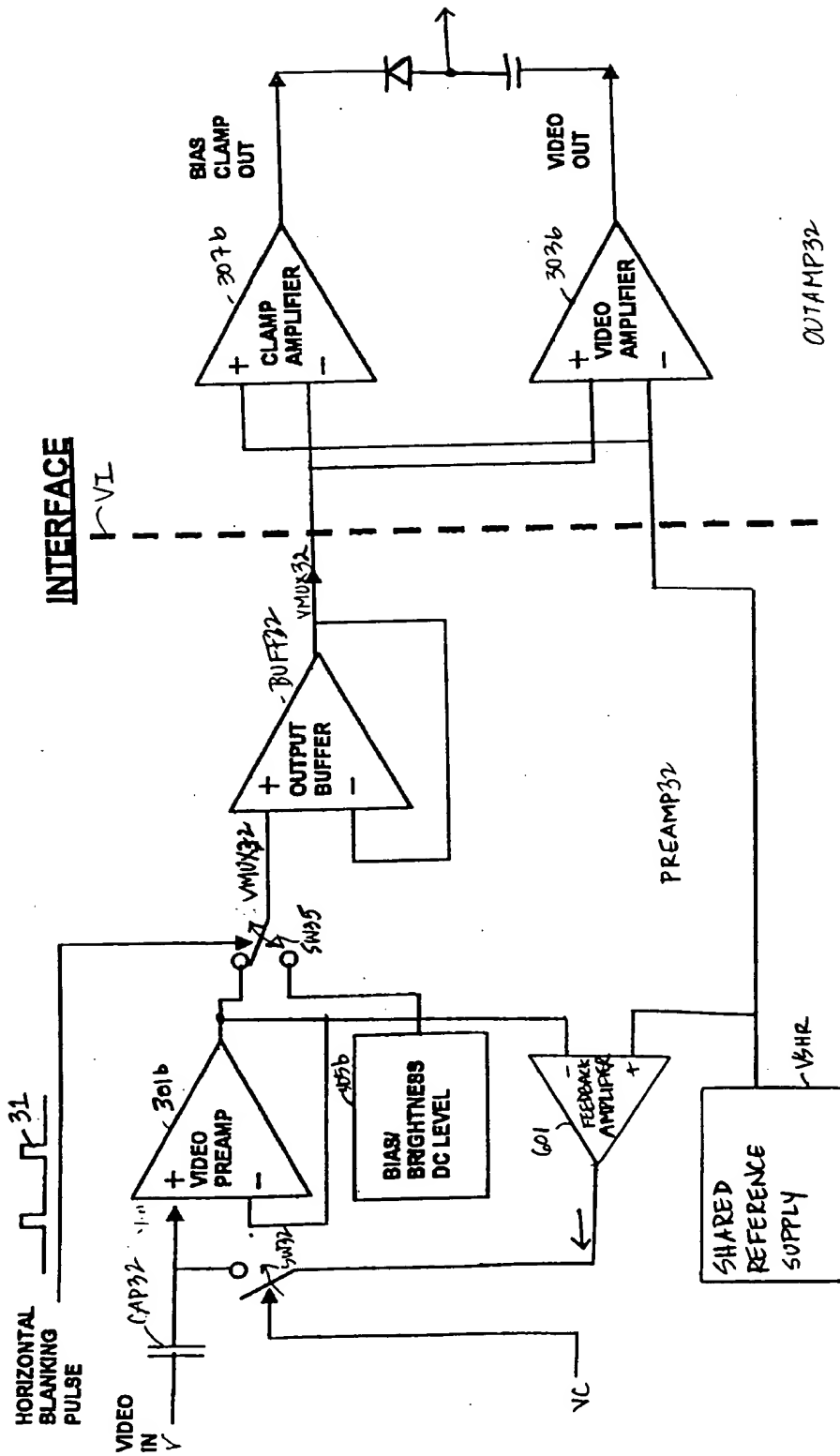


FIGURE 9

FIGURE 10

09698739-10300  
00/22T 6E/86960

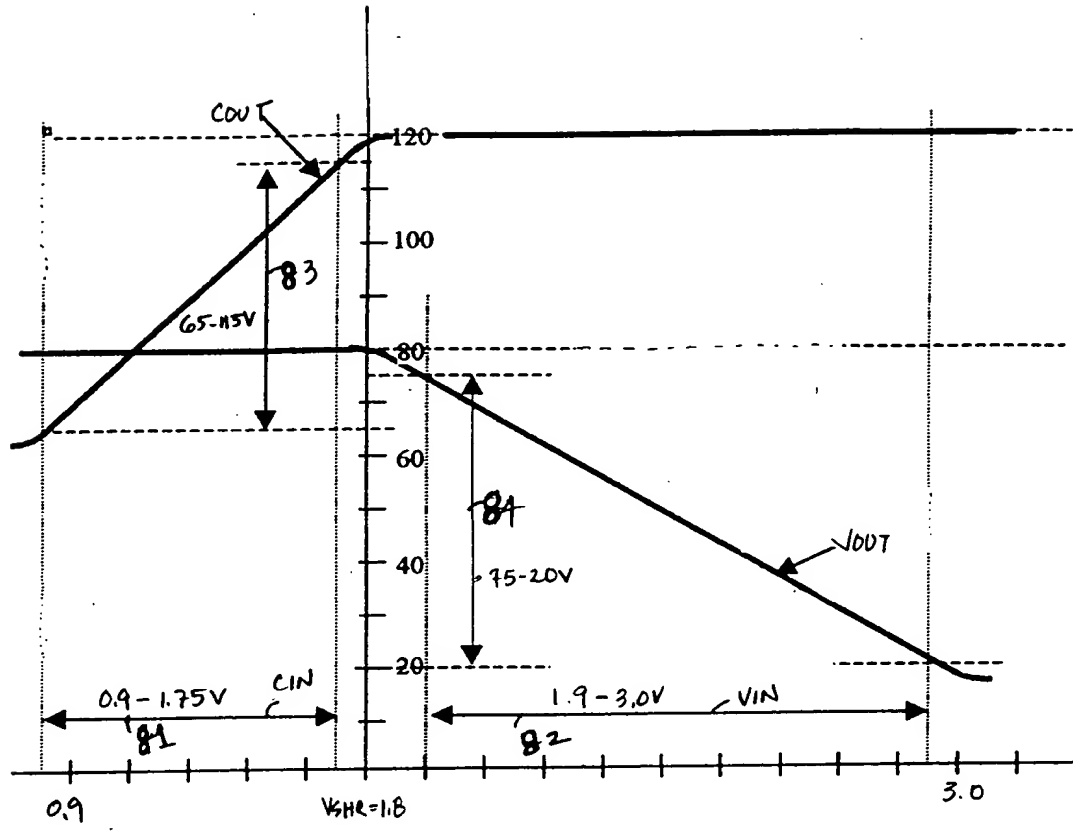


FIGURE 11

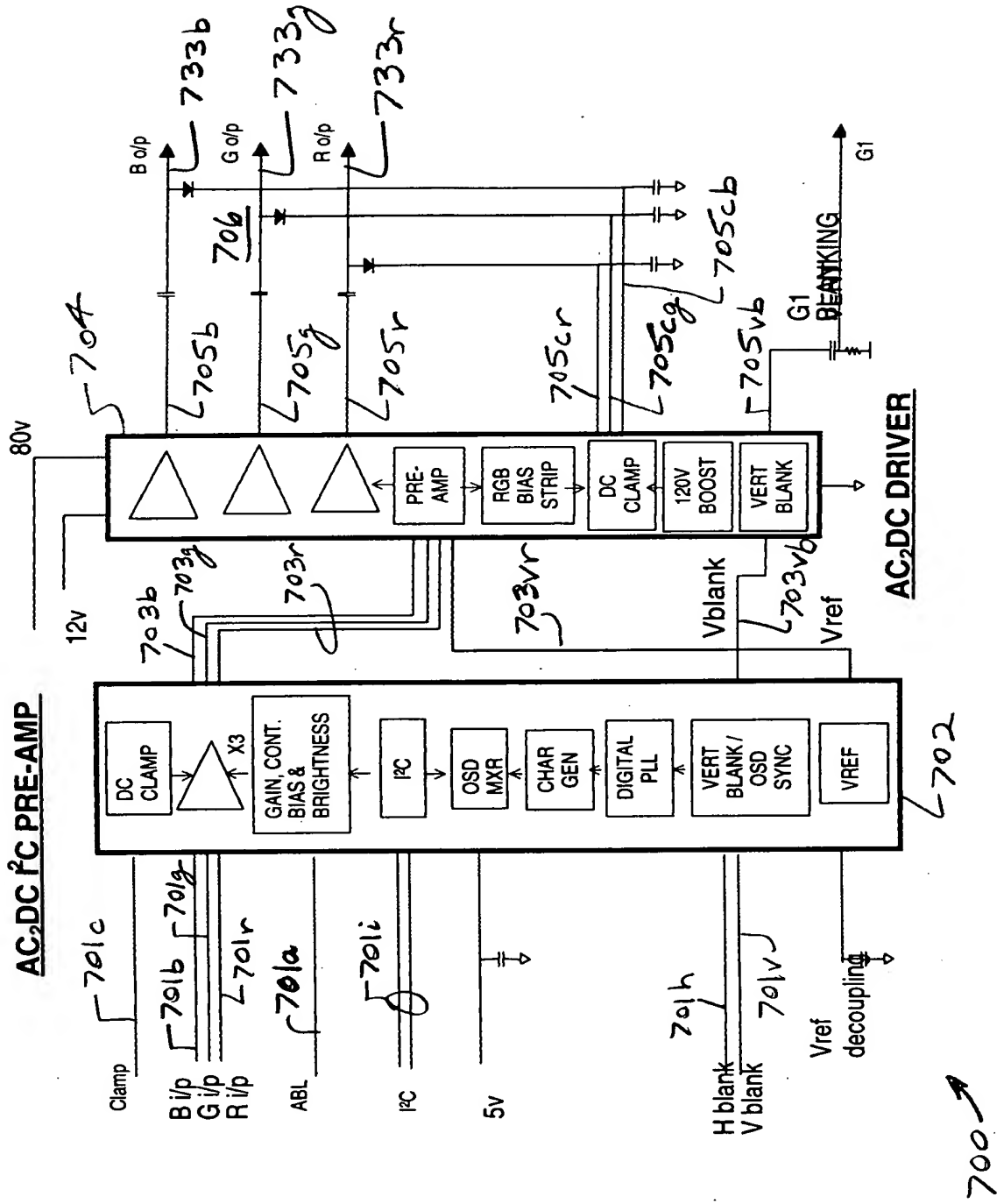
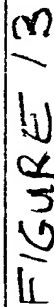


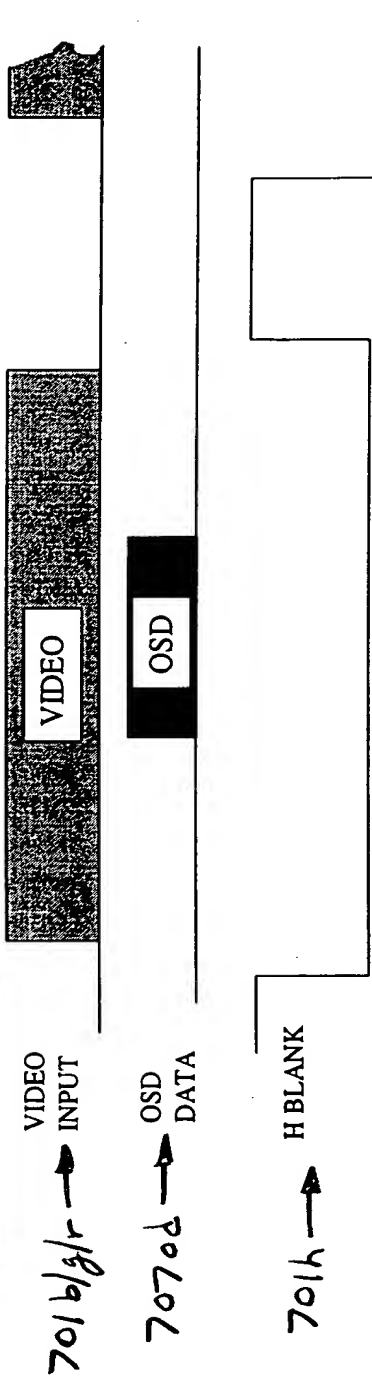
FIGURE 12





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# INPUT SIGNALS



# OUTPUT SIGNAL

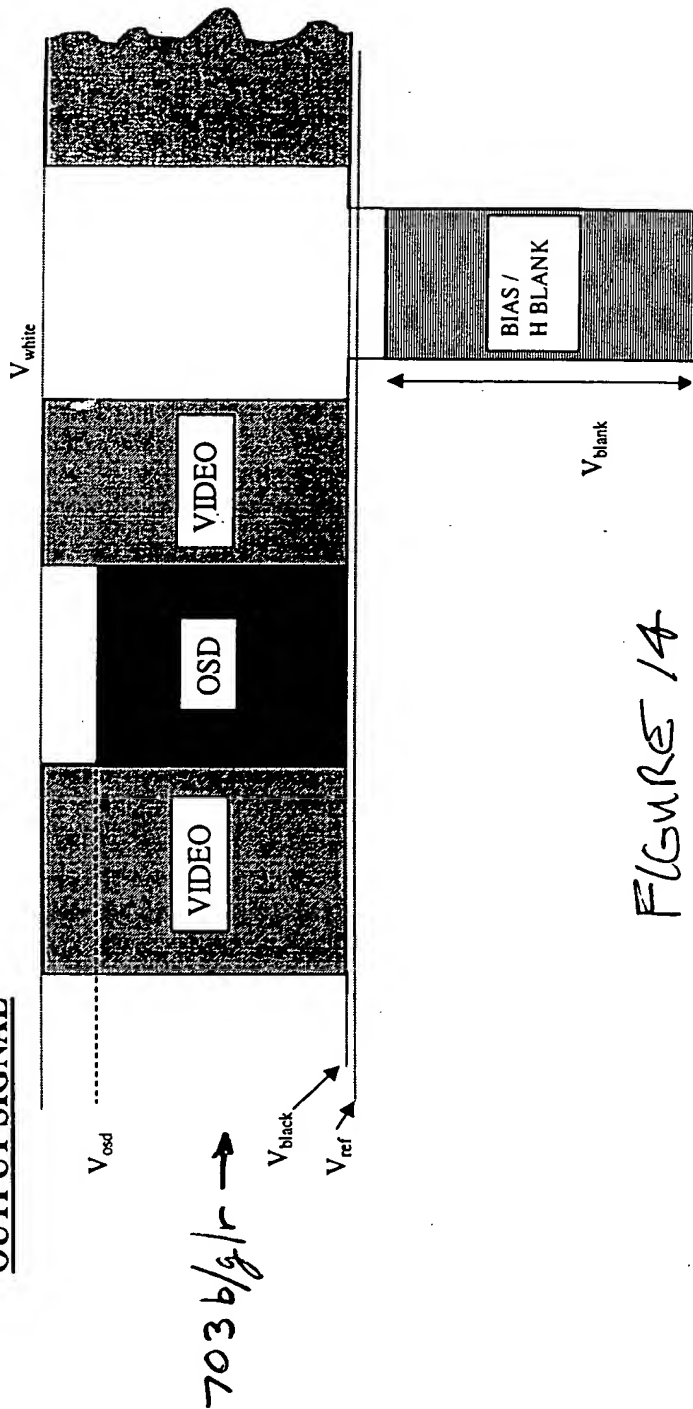


FIGURE 14

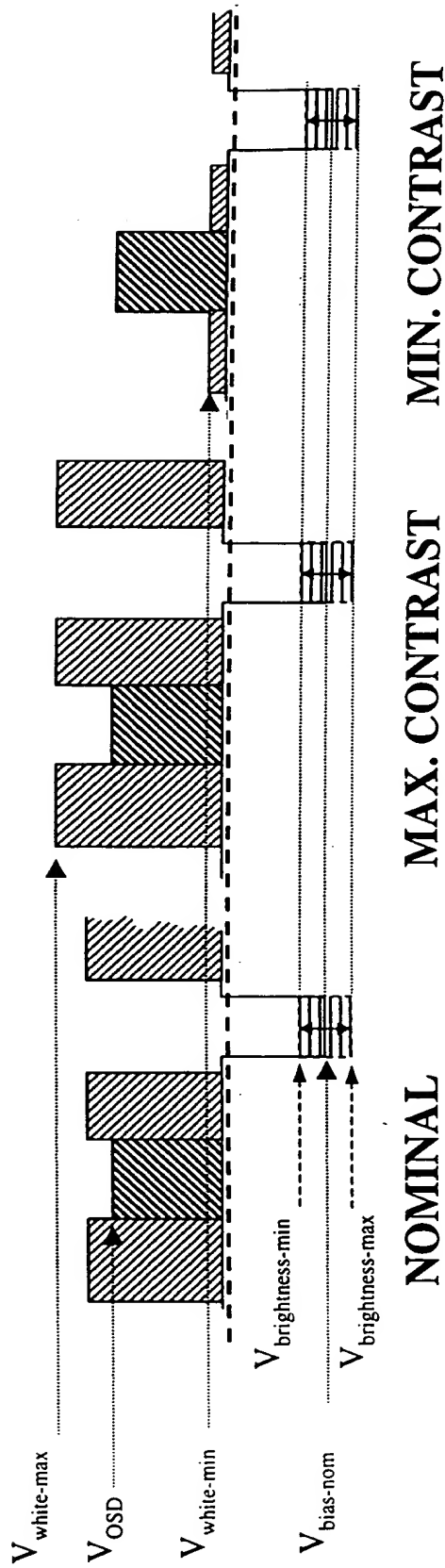


FIGURE 15

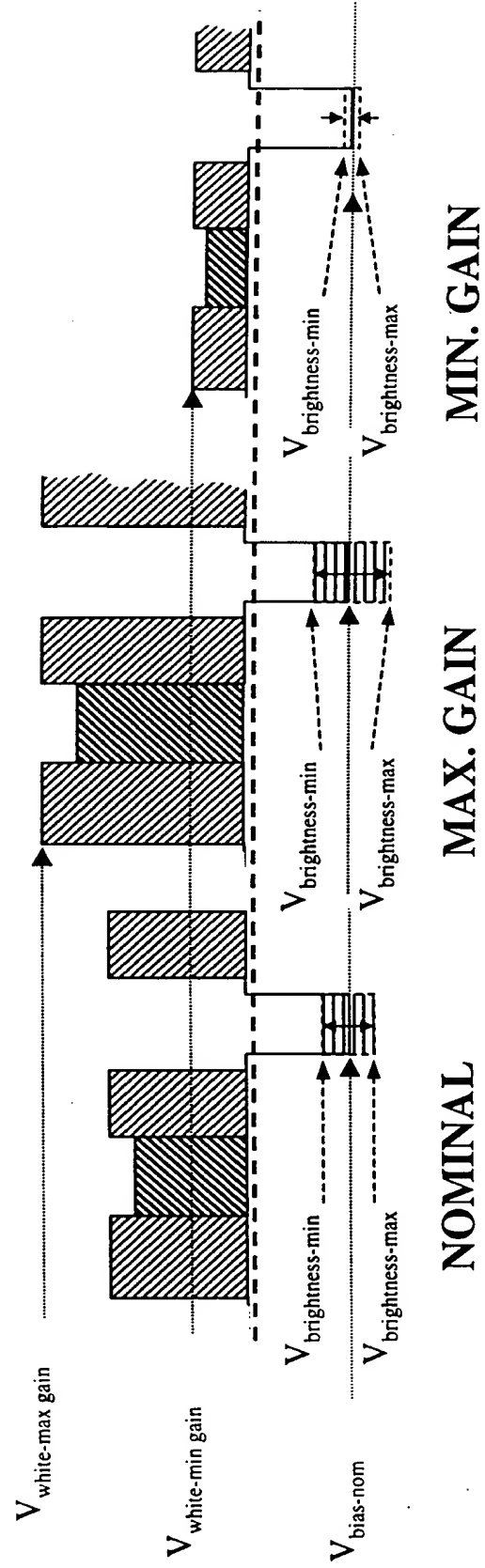


FIGURE 16



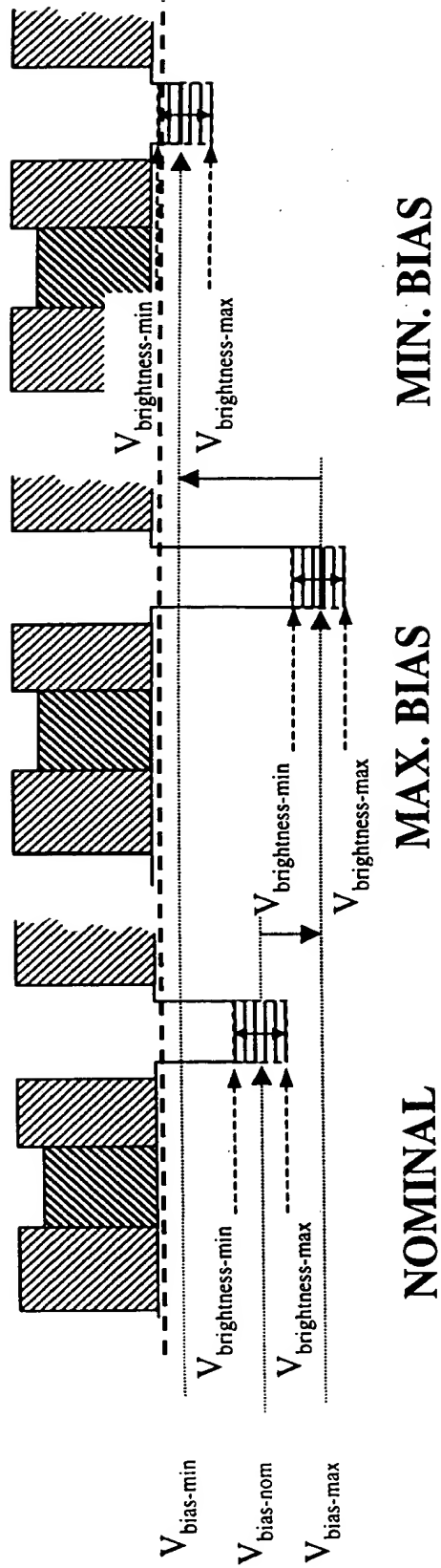


FIGURE 17

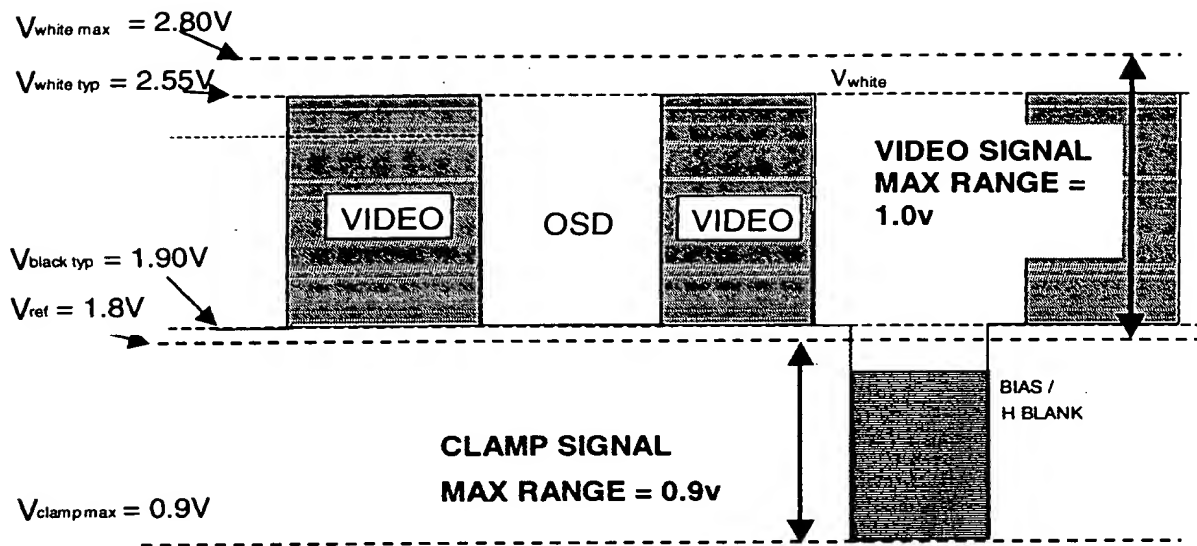


FIGURE 18

00/201" 6E/86960

7036/glr

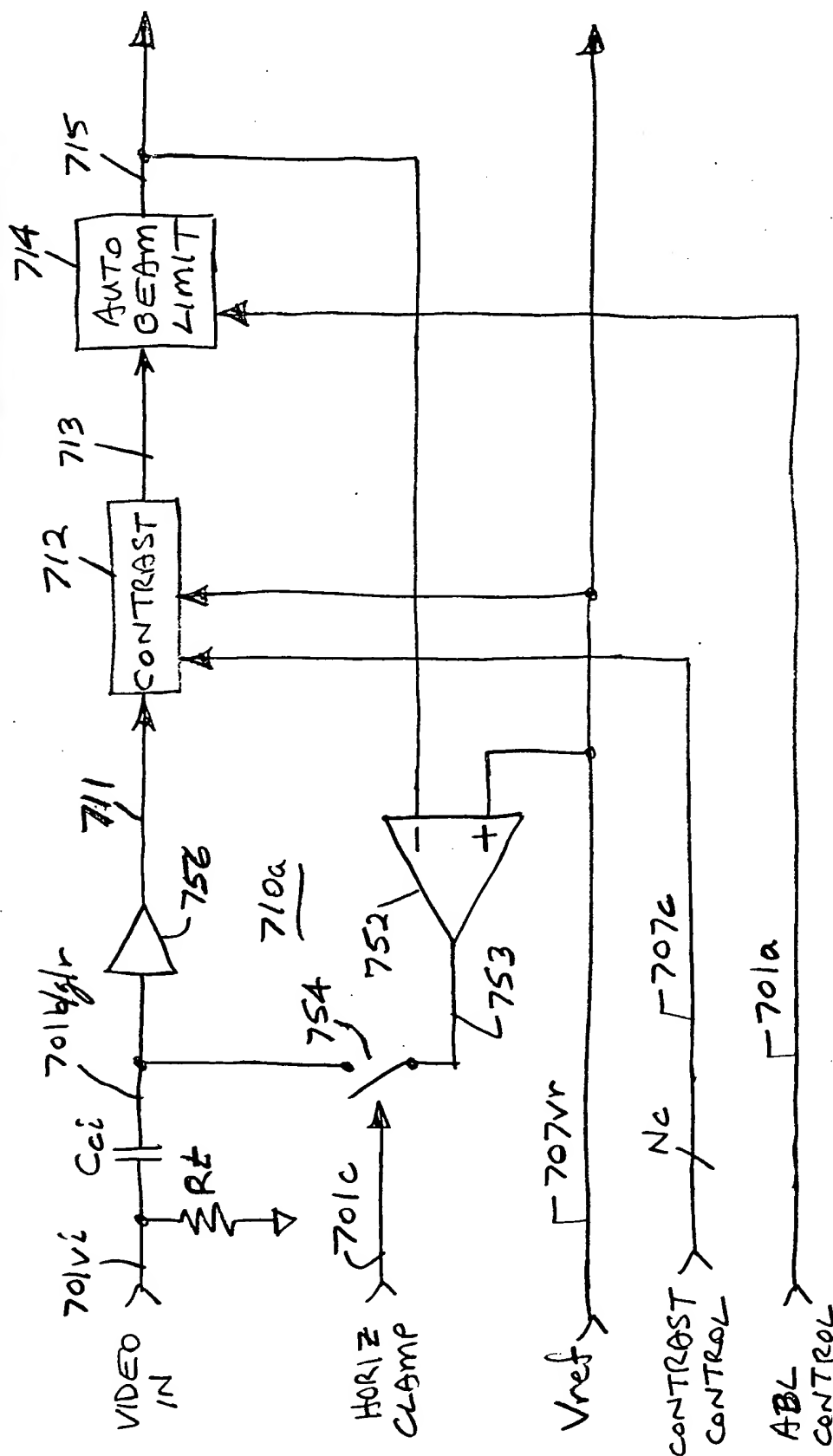


FIGURE 19

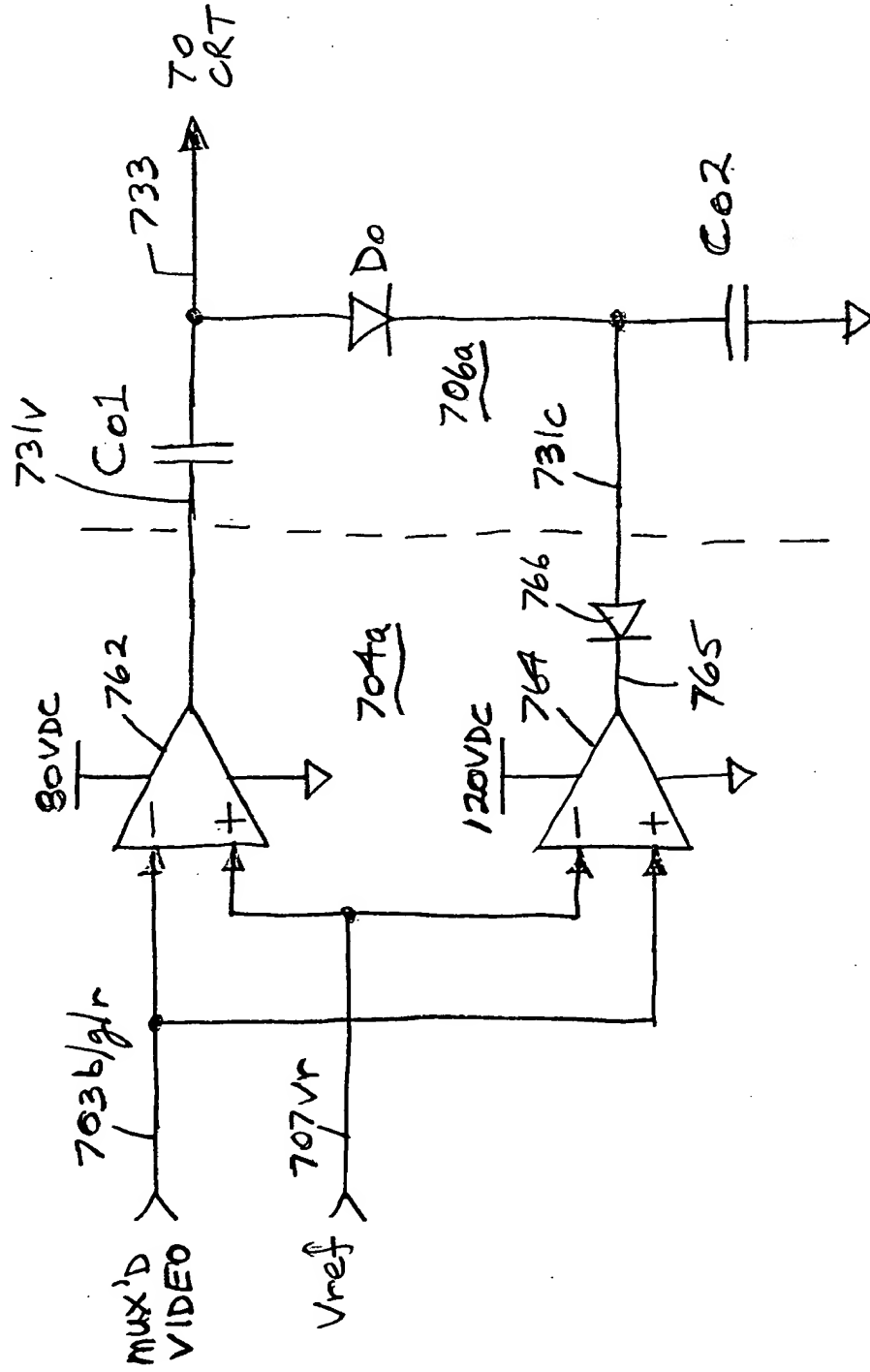
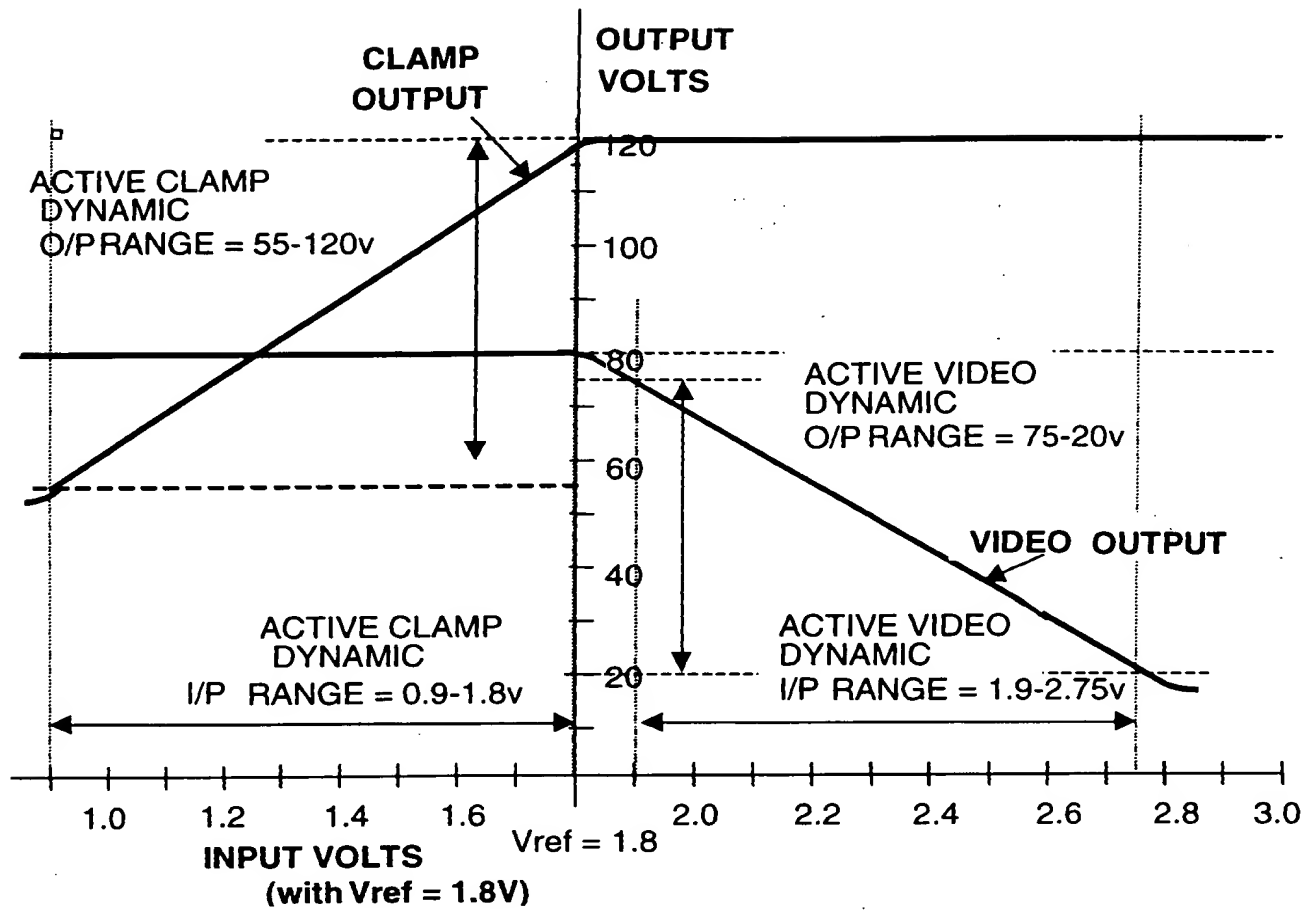


FIGURE 20



(Test Conditions:  $V_{ref} = 1.8v$ ,  $V_{cc1} = 80v$ ,  $V_{cc2} = 120v$ ,  $V_{bb} = 12v$ )

FIGURE 21

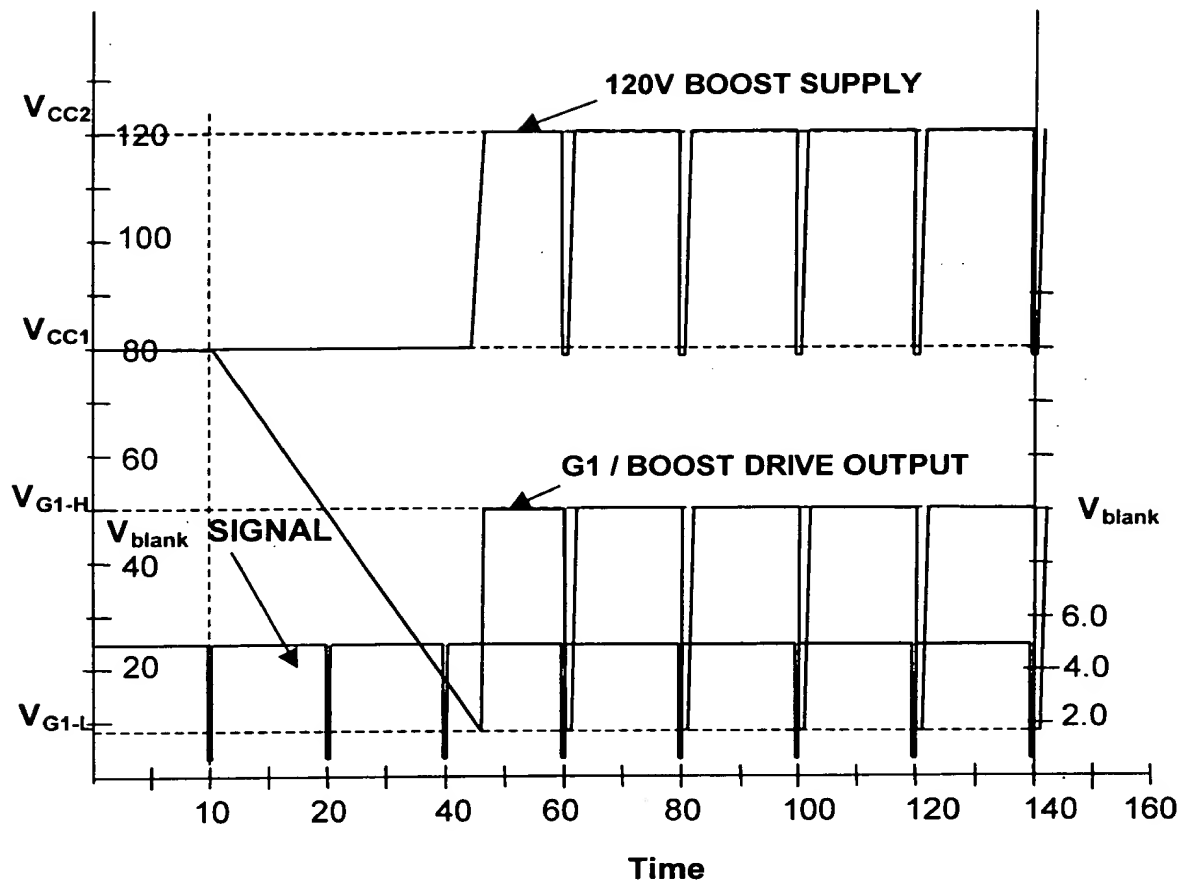
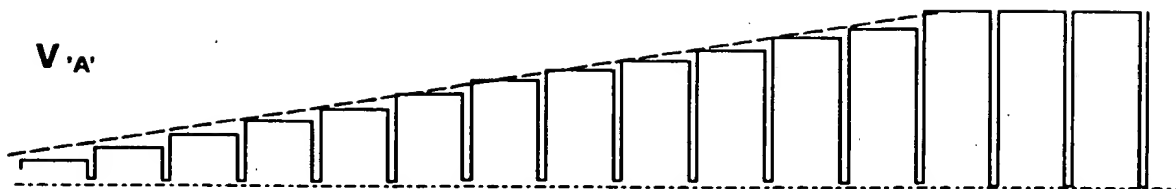


FIGURE 22

The diagram shows a VBLANK amplifier circuit. The input is a VBLANK INPUT signal, which is a series of pulses, labeled with handwritten '703vb'. This signal enters a VBLANK AMPLIFIER, represented by a triangle, which is labeled with handwritten '780'. The output of the amplifier is a signal labeled '705vb' that goes to node 'B'. Node 'B' is connected to a clamping network consisting of a diode D2 (pointing towards node 'B') and a capacitor C3 connected to a 120VDC source. A label 'Vcc2 (ie 120v) to Bias Clamps' points to this 120VDC source. Node 'B' is also connected to a resistor R1, which is connected to a V<sub>GRID1</sub> source. A capacitor C2 is connected between node 'B' and ground. A handwritten note 'Typically 40vp-p' with a double-headed arrow indicates the peak-to-peak voltage across C2. Node 'A' is connected to a diode D1 (pointing towards node 'A') and a capacitor C1 connected to a Vcc1 source (labeled 'ie 80v'). A current I<sub>c1</sub> is indicated flowing into node 'B' from the Vcc1 source through C1. The output of the circuit is GRID1.

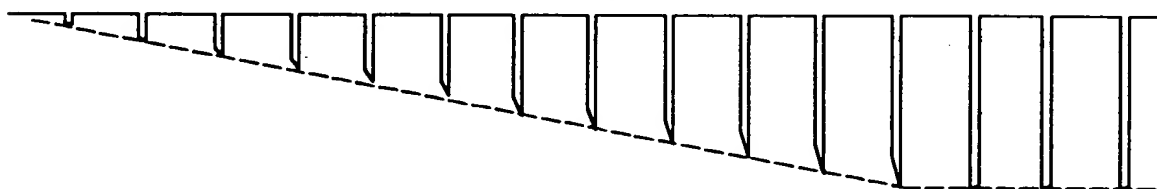
FIGURE 23

$V_{A'}$



LONG CHARGE UP TIME

$V_{B'}$



$I_{C1}$

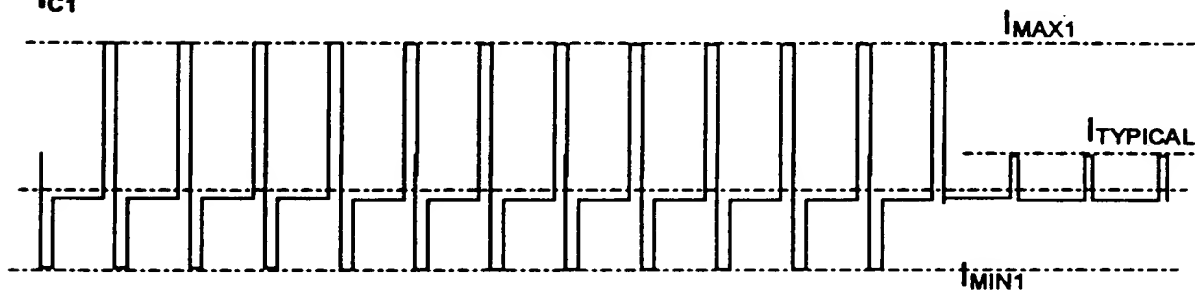
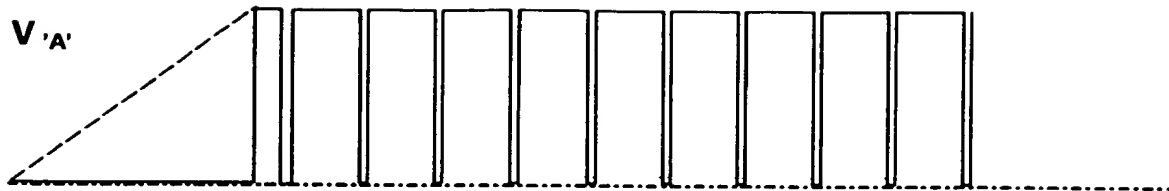


FIGURE 24



FIGURE 25



→  
**SHORT START UP TIME**

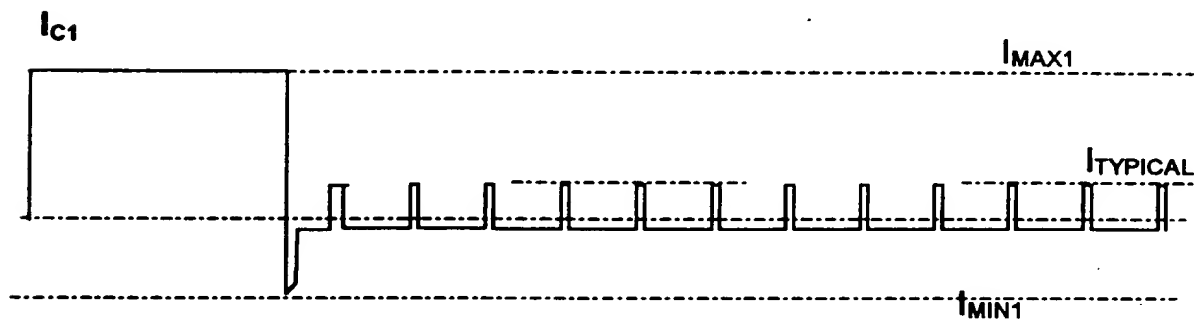
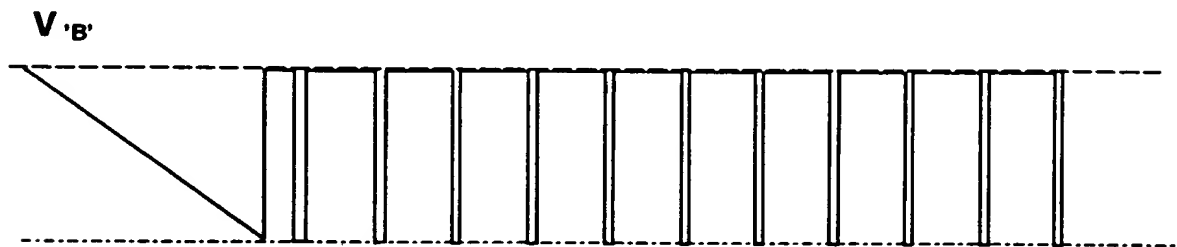
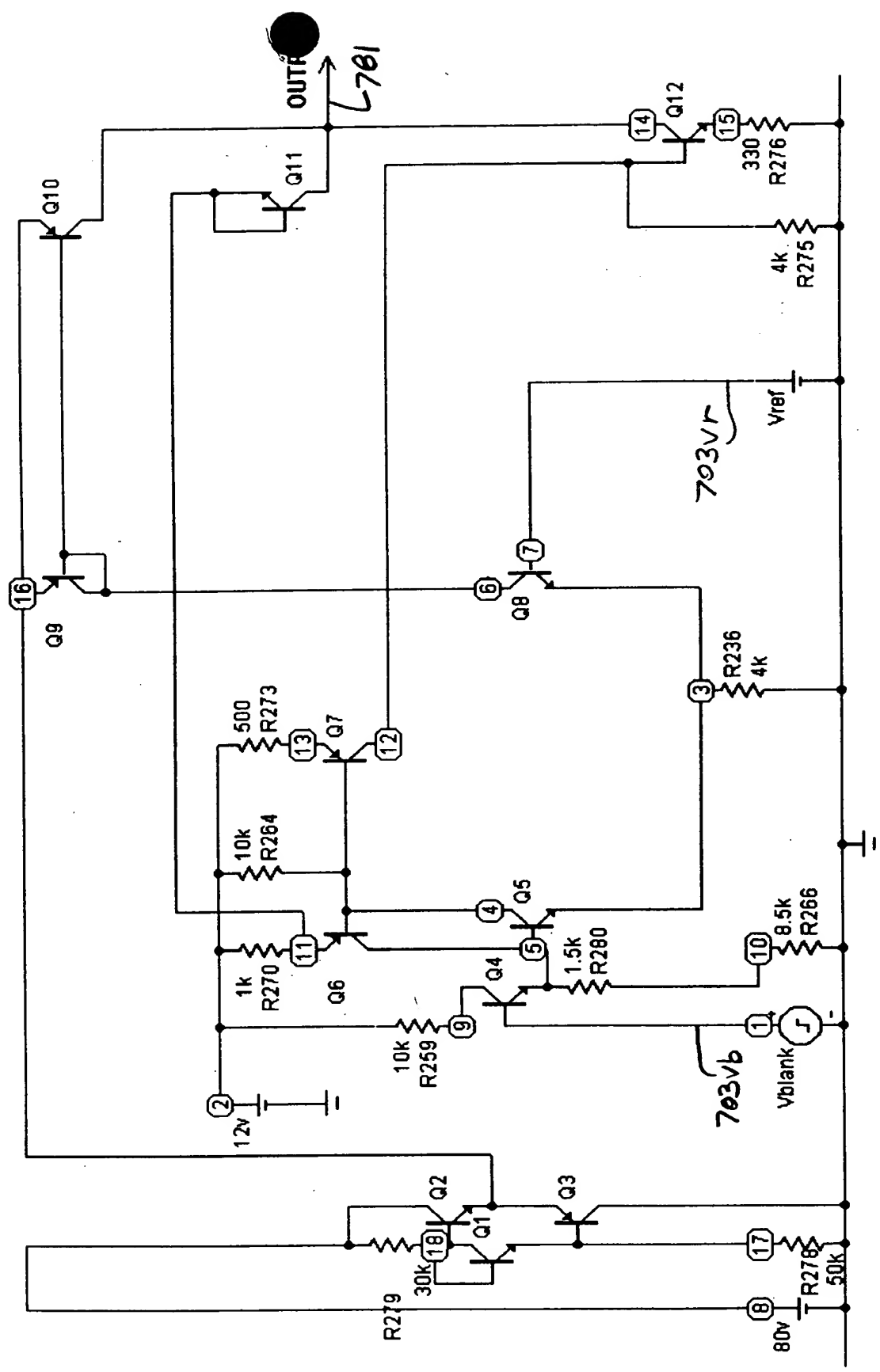


FIGURE 26

00/201" 6E/86560



700b

FIGURE 27

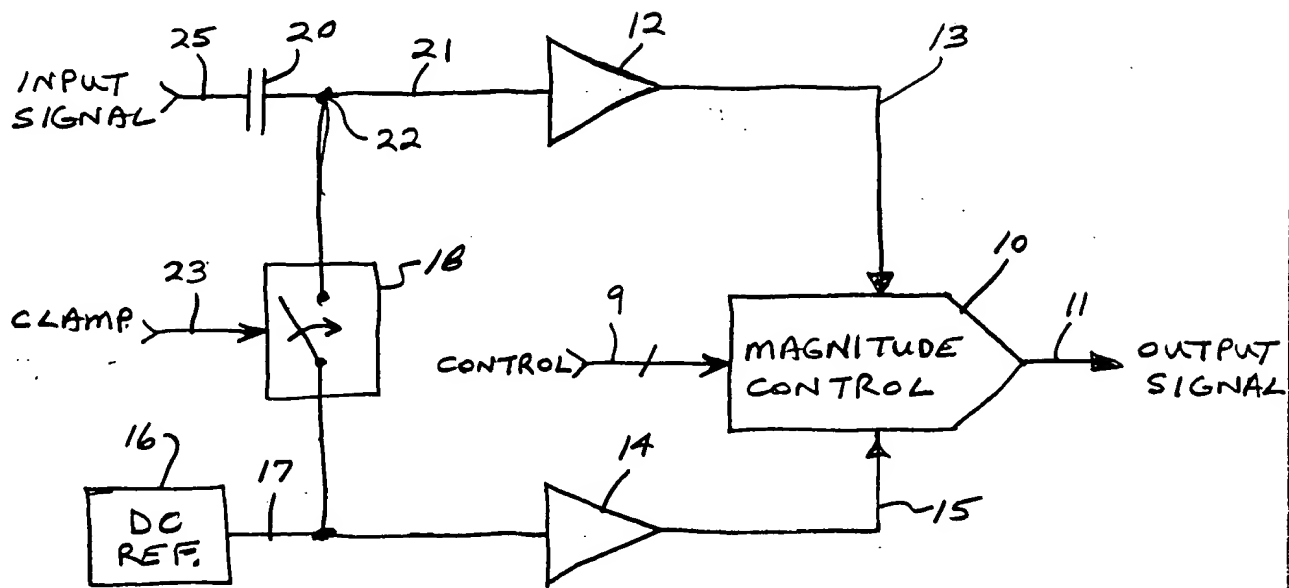


FIGURE 28

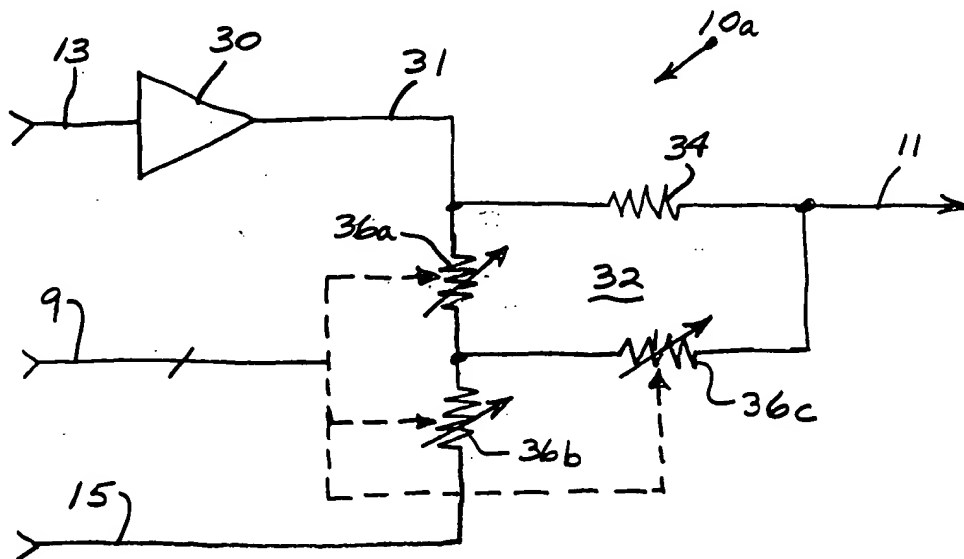


FIGURE 29

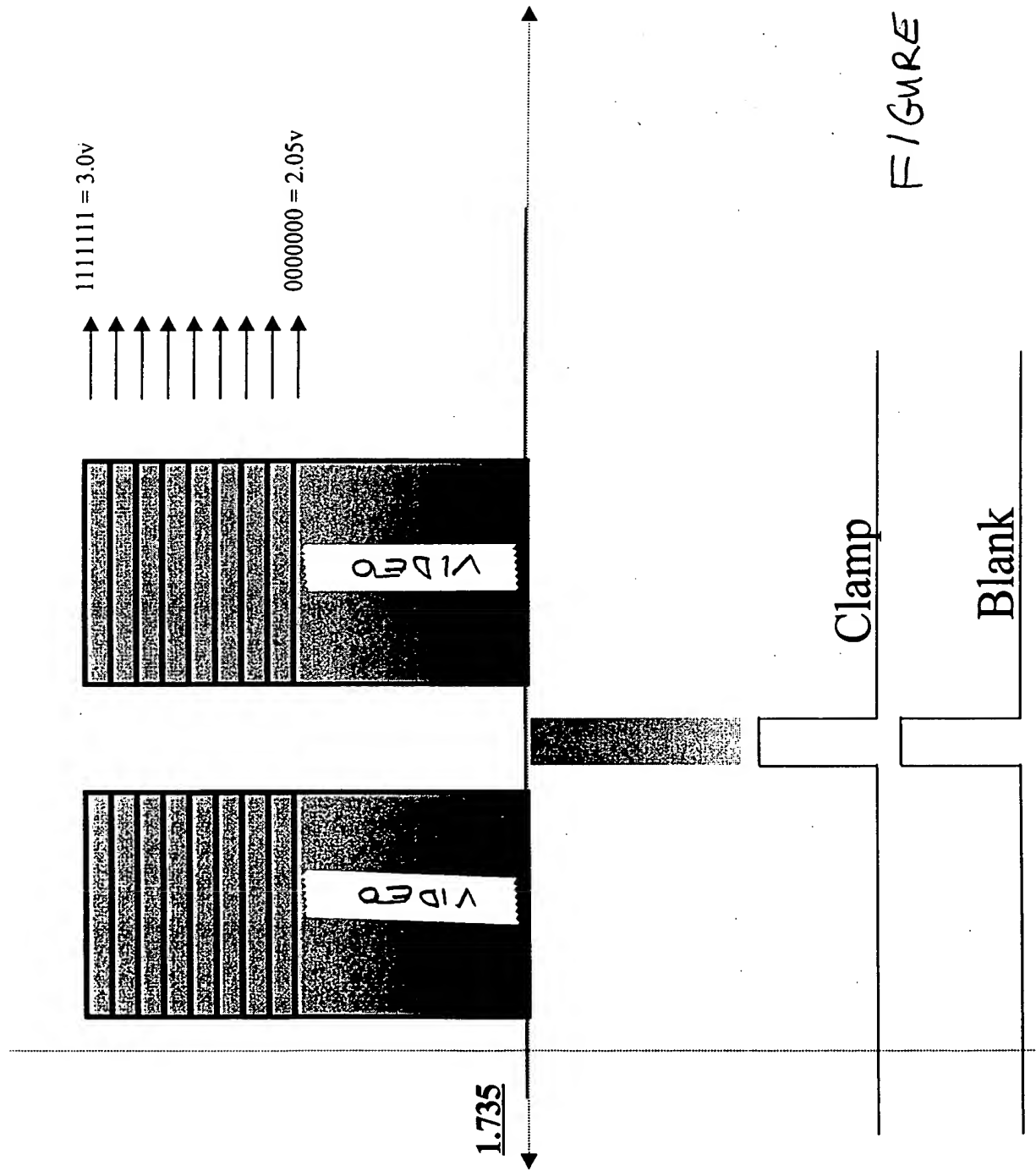


FIGURE 30

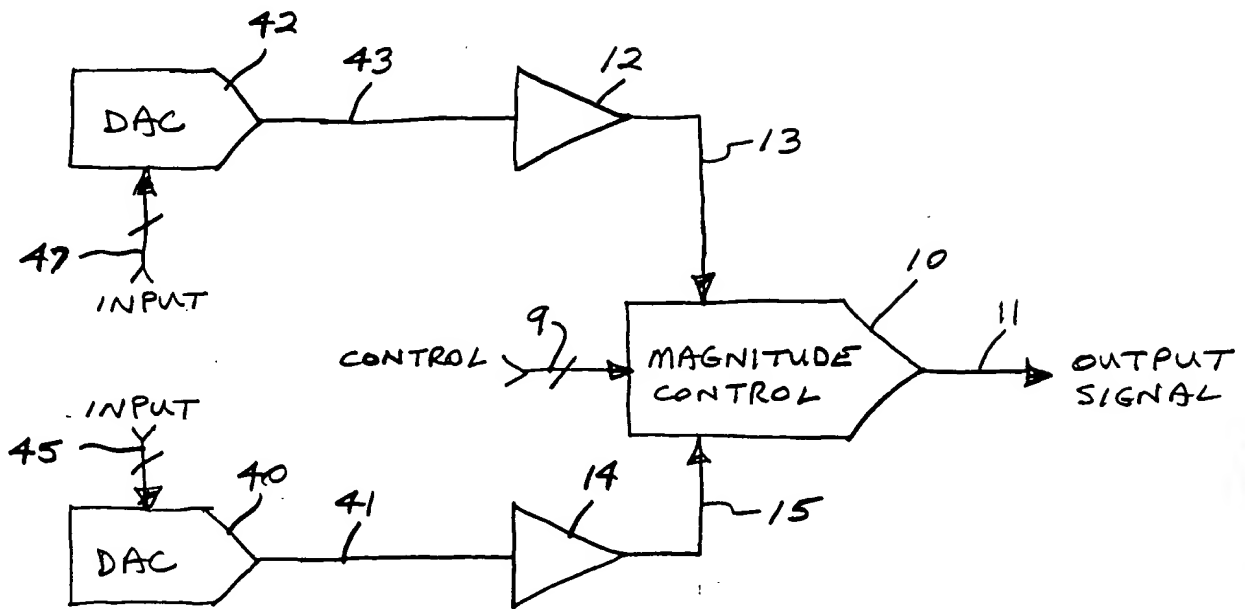


FIGURE 31

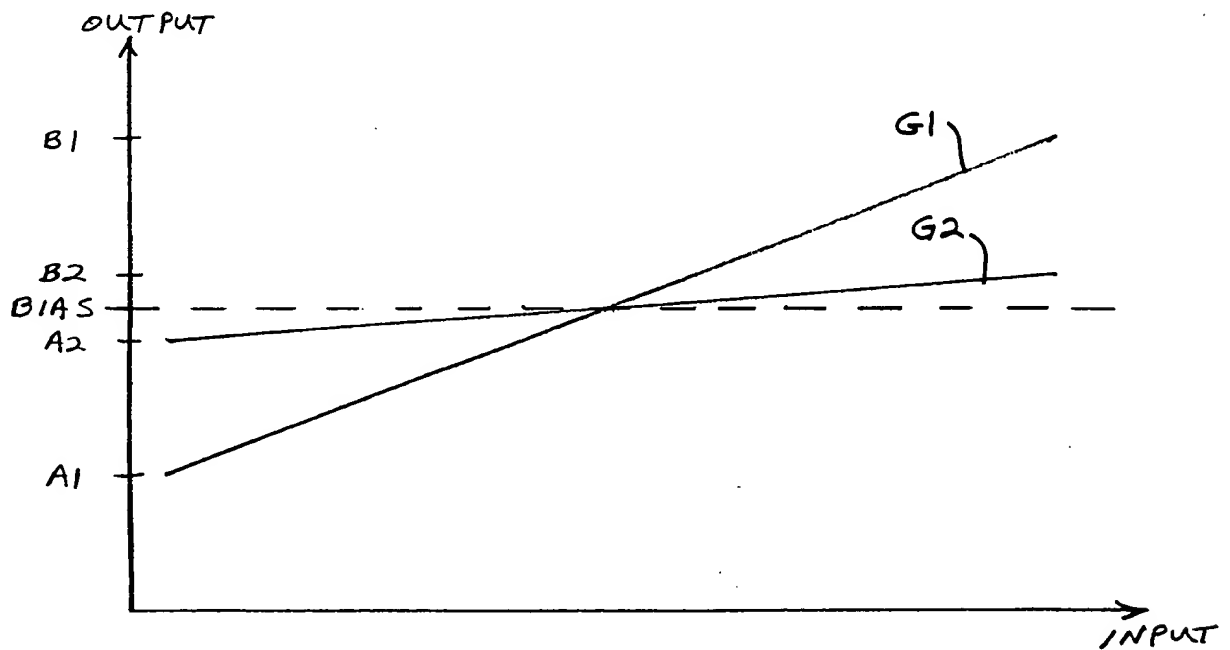


FIGURE 32

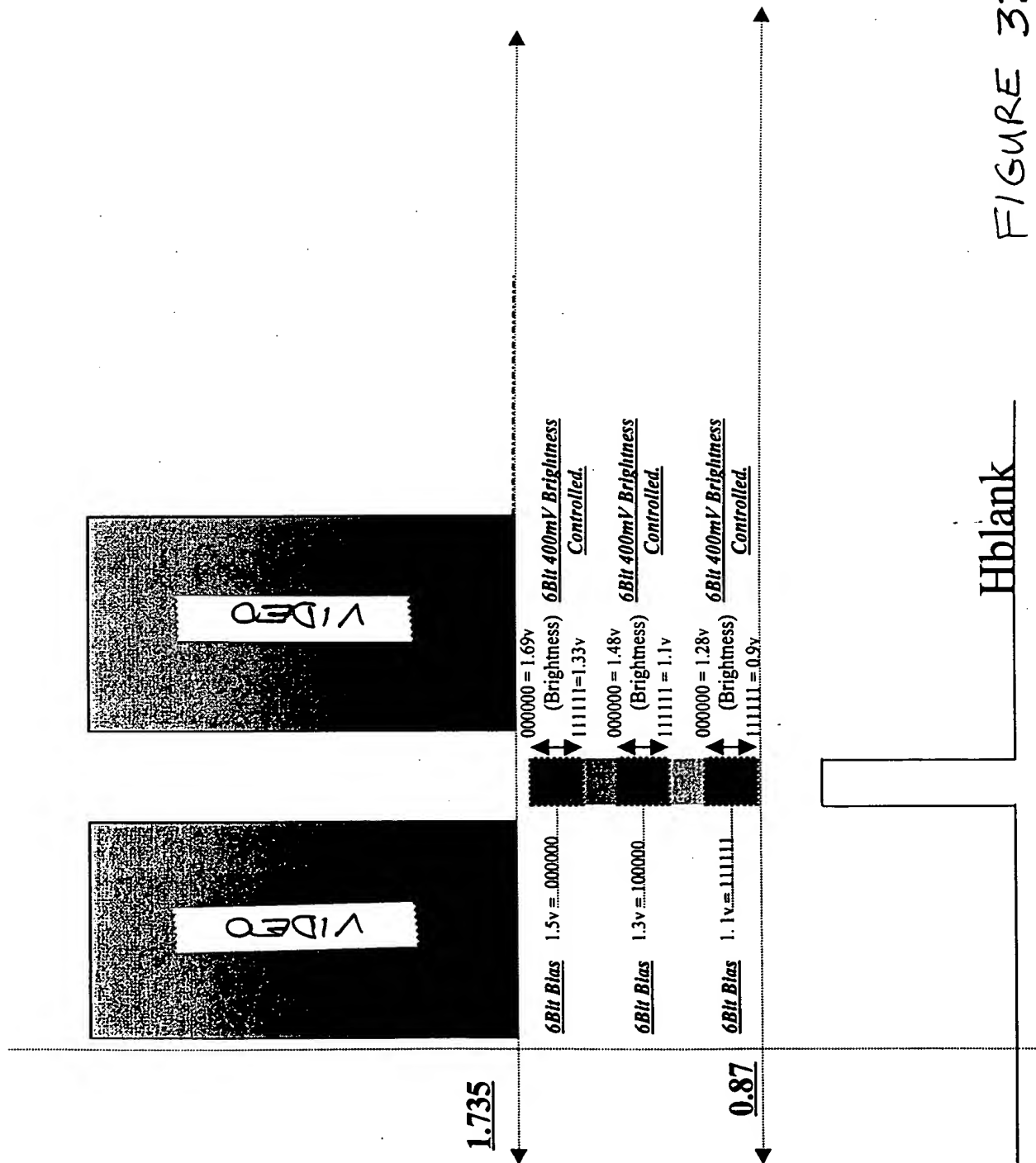


FIGURE 33

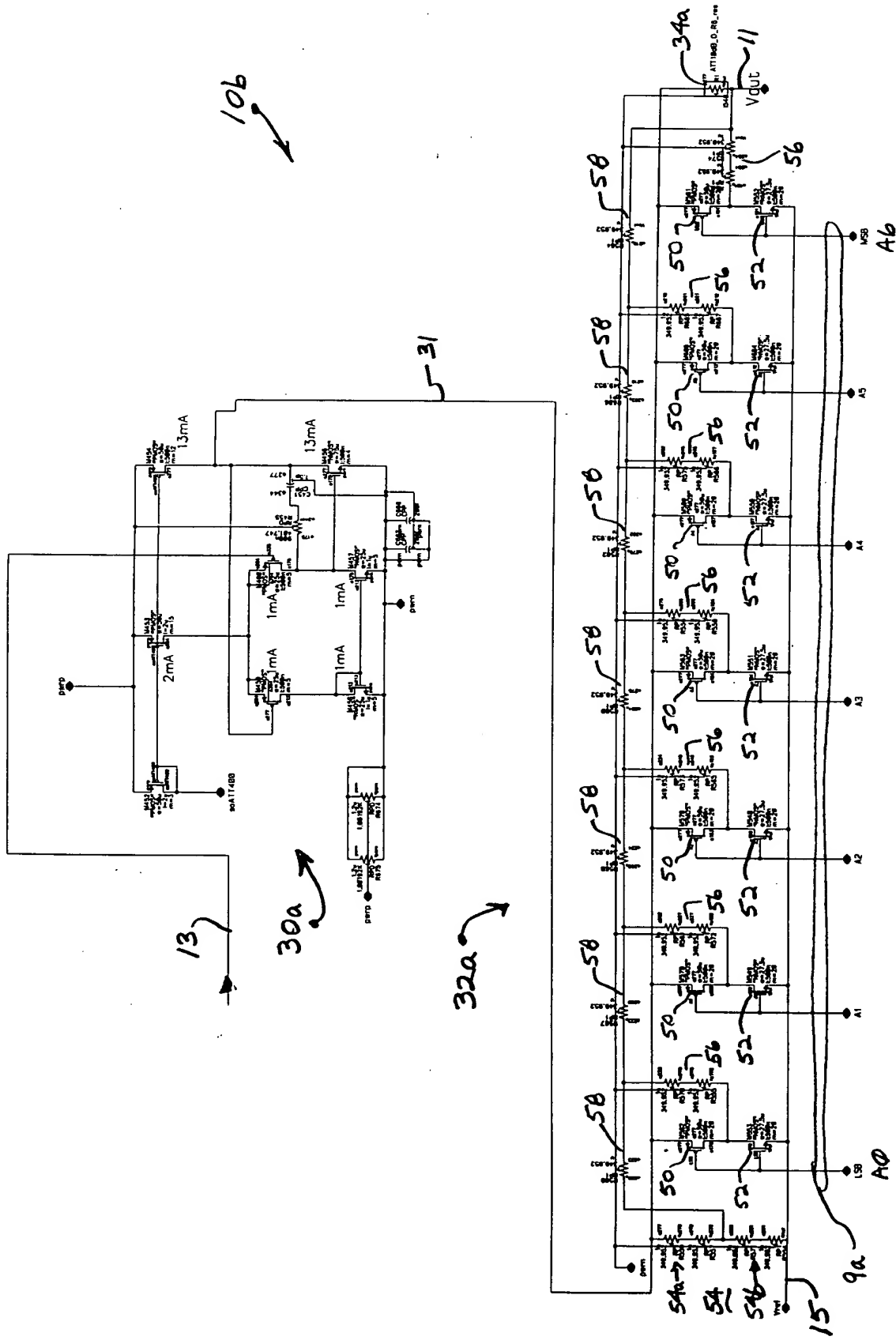


FIGURE 34



10c

32b

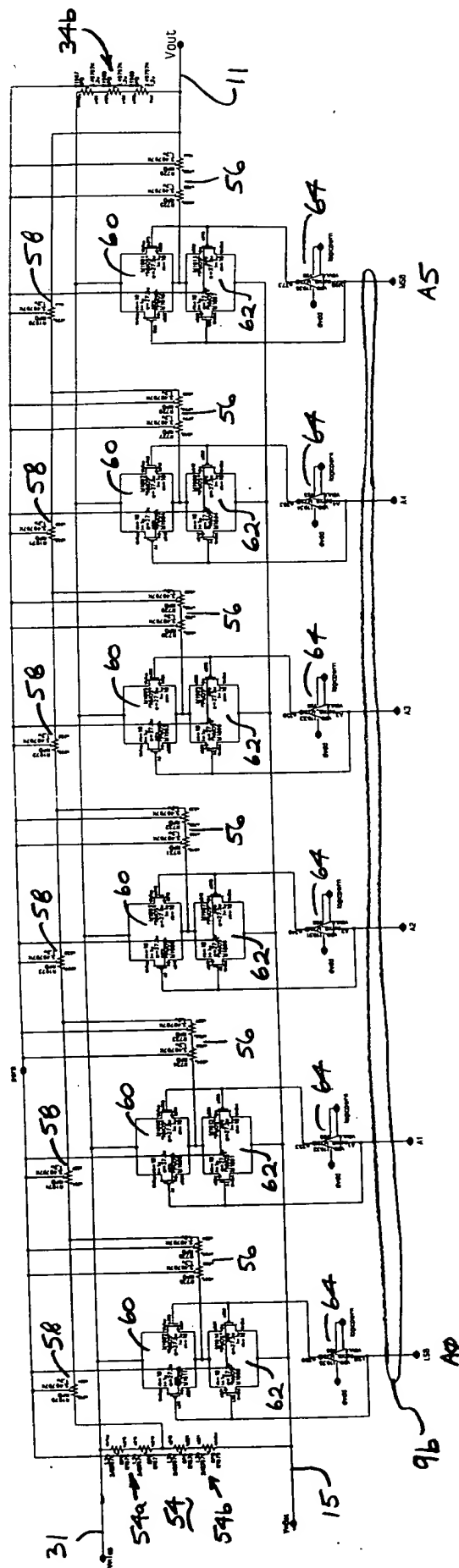


FIGURE 35

